Electronic Patent Application Fee Transmittal							
Application Number:	10709048						
Filing Date:	08-Apr-2004						
Title of Invention:		A MANUFACTURABLE METHOD AND STRUCTURE FOR DOUBLE SPACER CMOS WITH OPTIMIZED NFET/PFET PERFORMANCE					
First Named Inventor/Applicant Name:	Sadanand V. Deshpande						
Filer:	Frederick W. Gibb III/Kimberly Baumann						
Attorney Docket Number:	FIS920030397US1						
Filed as Large Entity							
Utility under 35 USC 111(a) Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							
Extension-of-Time:							

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
	Tot	810		